

## **AMENDMENTS TO THE SPECIFICATION:**

Page 8; please replace paragraph 0014 with the following amended paragraph:

[0014] For a better understanding of the invention, and to show by way of example how the same may be carried into effect, reference is now made to the detailed description of the invention along with the accompanying figures in which corresponding numerals in the different figures refer to corresponding parts and in which:

FIGURE 1a is an illustration of one embodiment of memory circuitry according to the present invention;

FIGURE 1b is an illustration of another embodiment of memory circuitry according to the present invention;

FIGURE 2 is a diagram depicting one example of a timing scheme for operation of the circuitry illustrated in FIGURE 1b in accordance with the present invention; and

FIGURE 3 is an illustration of another embodiment of memory circuitry according to the present invention; and

FIGURE 4 is an illustration of another embodiment of memory circuitry according to the present invention.

Page 15; please replace paragraph 0027 with the following amended paragraph:

[0027]In one alternative embodiment of circuitry 300, shown in FIG. 4, the activation of keeper circuitry 316 is signaled through a node 322 that is separate from node 318 – similar to circuit 120. In this embodiment, the gate of keeper 316 is coupled to a separate node 322 that provides a sleep mode control signal (SLK) for keeper 316. The sleep mode control signal is independent of, and not concurrent with, the sleep mode control signal applied to node 318 for the wordline driver circuitry.